ATTY DOCKET NO. ITL.1028US (P16764)		SERIAL NO. 10/661,079
INFORMATION DISCLOSURE CITATION (use several sheets if necessary)	APPLICANT(S): MICHAEL W. MORROW	
	FILING DATE: September 12, 2003	GROUP ART UNIT: 2183

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
✓	A.	6,463,522	10/08/2002	AKKARY	712	216	
✓	B.	6,016,542	12/18/2000	GOTTLIEB ET AL.	712	225	
	C.						
	D.						
	E.						
	F.						
	G.						
	H.						
	I.						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	J.							
	K.							
	L.							
	M.							
	N.							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

✓	O.	Marr, Deborah T. ,et al. "Hyper-Threading Technology Architecture and Microarchitecture" http://developer.intel.com/sites/corporate/tradmarx.htm. Intel Corporation, February 2002.
✓	P.	"Introduction to Hyper-Threading Technology" http://www.intel.com/technology/hyperthread/download/25000802.pdf. Intel Corporation, 2001.
✓	Q.	"Intel® XScale™ Microarchitecture for the PXA255 Processor-User's Manual" Table of Contents, and Appendix A. http://www.intel.com/design/pca/applicationsprocessors/manuals/27879601.pdf. Intel Corporation, March 2003.
	R.	
	S.	
	T.	
	U.	

EXAMINER		DATE CONSIDERED

PTO-1449   Page 1 of 1